



THOTA. VENKATA LAKSHMI

Department: Electronics and Communication Engineering

College: Seshadri Rao Gudlavalleru Engineering College

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D. No: 5-58-1,

Sai Nagar,

Near Market Yard,

Gudivada- 521 330, Krishna (dist).

ACADEMIC QUALIFICATIONS:

S. No	Name of the Degree	University	Specialization	Year of Pass
1	PhD	JNTU Kakinada	Low Power VLSI Design	pursuing
2	M. Tech	JNTU Kakinada	Instrumentation and Control Systems	2009
3	B. Tech	Acharya Nagurjana University, Guntur	Electronics and Communication Engineering	2002

4	Diploma	S.B.T.E.T.	Electronics and Communication Engineering	1999
5	S.S.C	St.Anthony's RCM High School, Vijayawada	----	1996

Patents Grant (on PhD Work) : 01-----under examination

Patents Published: 02

1. A patent with Application no. 202441003531 was published by T.Venkata Lakshmi and Dr.M.Kamaraju for an invention entitled “**FinFET-Based Single Transistor Single Diode SRAM Cell for Ultra-Low Power Biomedical Implantable Systems**”, on 09th February 2024.
2. A patent with Application no. 202241061751A was published by Dr.Y.Rama Krishna, Dr. M. Kamaraju, Dr. V.V.K.D.V Prasad and Mrs. T. Venkata Lakshmi for an invention entitled “**Electromagnetic Seperating Means of a Smart Trolley for Multiple Compartments and Method Thereof**” on 30th October, 2022

Design Patents Submitted: 02

1. Title of Invention: Novelty in the shape & configuration of the **Heat Sink**: Dated: 24/01/2025.
2. Title of Invention: Novelty in the shape & configuration of the **Wheel Chair**: Dated:15/01/2025.

PROFESSIONAL EXPERIENCE

Designation	Institution	Working Period	
		From	To
Associate Professor	Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru	Feb 2009	Till date
Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	August 2003	January 2009

PROJECTS GUIDED:

Ph.D	Post Graduation	Under Graduation
NIL	10	30

SPECIALIZATION: LOW POWER VLSI DESIGN

RESEARCH PAPERS/BOOKS:NIL

PAPERS PUBLISHED IN JOURNALS: 19

1. M.V. Srikanth, A. Suneel Kumar, B. Nagasirisha, **T. Venkatalakshmi**, “Brain MRI and CT Image Fusion Using Multiscale Local Extrema and Image Statistics”, ECTI Transactions on Electrical Engineering, Electronics, and Communications, Vol.22, No.1 February 2024. **(Free Scopus)**
2. **Lakshmi, T. V.**, and Kamaraju, “A Novel Single Ended 3T SRAM Cell Using FinFET Technology For Low Power Applications”. i-Manager's Journal on Electronics Engineering, 2024/9/1, Volume 14, Issue 4.**(Free Journal)**
3. Srikanth M, V; Kumar, A Suneel; Nagasirisha, B; Lakshmi, **T Venkata Lakshmi**, “Medical Image Fusion using Multi Scale Guided Filtering”. i-manager's Journal on Pattern Recognition; Nagercoil Vol. 11, Iss. 1, (Jun 2024): 13-23, DOI:10.26634/jpr.11.1.20735 **(Free Journal)**
4. A. Suneel Kumar, M.V. Srikanth, B. Nagasirisha, **T. Venkatalakshmi**, “Design and Evolution of High Gain, Ultra Wide Band Antenna for 5G Applications”, International Journal Of Microwave & Optical Technology, Sep 2023, Vol. 18 Issue 5, P490-497. **(Scopus).**
5. **Lakshmi, T. V.**, and Kamaraju, M. “A Novel 1T-1D Single Ended SRAM Cell using FinFET Technology for Low Power Applications”. i-manager’s Journal on Electronics Engineering, 2024/6/1, 14(3), 6-18. DoI.org/10.26634/jele.14.3.20286 .**(Free Journal)**
6. Srikanth M,Nagasirisha, B,Kumar A Suneel, **T Venkata Lakshmi** “Magnetic Resonance and Computer Tomography Image Fusion Using Novel Weight Maps Obtained by Using Median and Guided Filters” i-Manager’s Journal on Image Processing, vol. 11, Issue. 2, p. 17–26, 2024. DOI 10.26634/jip.11.2.20715.**(Free Journal)**
7. B. Nagasirisha , M.V. Srikanth, A. Suneel Kumar, **T. Venkatalakshmi**, “Multiscale Weighted Mean Curvature Filter and Interval Type-2 Fuzzy Based Brain Image Fusion”,

International Journal of Modern Education and Computer Science (IJMECS) (**Free Scopus**) (**Under Review**)

8. **Lakshmi, T. V.,** and Kamaraju, M.”1T-1D Single-Ended SRAM Cell Design for Low Power Applications Using CMOS Technology” Distributed Computing and Optimization Techniques: Select Proceedings of ICDCOT 2021, Springer Nature Singapore, 2022/9/12 (**Scopus**).
9. **Lakshmi, T. V.,** and Kamaraju , “A review on SRAM memory design using FinFET technology”, International Journal of System Dynamics Applications (IJSDA), Publisher-IGI Global, Volume 11, Issue 6, Pages1-21.(**ESCI**)
10. Surapaneni Aparna, **T. Venkata Lakshmi,** M.Kamaraju, Y. Sri Chakrapani, “Implementation of Deep Learning Accelerator Unit”, International Journal of Engineering Research & Technology, ISSN: 2278-0181, Vol.8, Issue: 7 July-2019.
11. Pallavi Sesham, VenkatalakshmiThota, Kamaraju M, “Design Of Kogge-Stone Adder Using Adiabatic Logic”, International Journal of Advances in Electronics and Computer Science, ISSN: 2393-2835 Volume-5, Issue-6, Jun.-2018.
12. S.Pallavi, T.Venkata Lakshmi, M. Kamaraju, “Implementation and comparative analysis of Carry Select Adder using Kogge-Stone Adder in Adiabatic logic” Journal of Engineering Research and Application, UGC approved Journal Research article, ISSN : 2248-9622, Vol. 8, Issue 6 (Part - III) June 2018, pp 22-26.
13. G.Rajesh,T.Venkata Lakshmi, M.Kamaraju,“Low Power VLSI implementation of error detection and correction codes using OLS codes” International Journal of Computer Science and Information Technology & Security (IJCSITS), ISSN: 2249-9555 Vol.6, No.4, July-August 2016.Impact Factor:3.765
14. V. Sreelakshmi, **T. Venkata Lakshmi,** M. Kamaraju “A Low Power delay buffer using Ring counter and Gated driver” International Journal of Engineering & Science Research (IJESR), Vol-5, Issue-12, Dec. 2015ISSN 2277-2685, pp: 1447-1455.
15. A. Haritha, Y. Bhavani Prasad, M. Kamaraju, **T. Venkata Lakshmi** “Analysis of Memristors with and without Temperature Effects” International Journal of Applied Engineering Research (IJAER), Volume-10, number-20, ISSN: 0973-4562, 2015, pp: 41464-41470. (**WOS/SCOPUS**)

16. I. Srivalli, **T. Venkata Lakshmi**, M. Kamaraju “ Design of an Efficient Router in Networks” International Journal of Engineering in Advanced Research Science and Technology (IJEARST), Volume-01, issue-01, October 2015.
17. Sravanthi, **T. Venkata Lakshmi** “VLSI Implimentation of ALU using Reversible Logic with Vedic Mathematics” International Journal of Scientific Engineering and Technology Research (IJSETR), Volume. 04, Issue. 01, ISSN: 2319-8885, January-2015, pp: 0142-0146.
18. P.V. SubbaRao, **T. Venkata Lakshmi**, M. Kamaraju, “A Novel Voting System Using SMS” International Journal of Computer Science & Information Technology (CS & IT) (DOI: 10.5121/csit.2013.3634), August 2013, PP: 297-303. Impact factor: 2.93
19. J. Tulasi, **T. Venkata Lakshmi**, M. Kamaraju, “FPGA Implementation of Convolution Encoder and Hard Decision Viterbi Decoder” International Journal of Computer Communication Technology (IJCCT), Volume-3, Issue-4, ISSN (online): 2231-0371, 0975-7449, 2012.(SCOPUS)

Research IDs

Research Ids: Papers available at the following ids	
Google Scholar	https://scholar.google.com/citations?view_op=list_works&hl=en&hl=en&user=fMm5SbIAAAAJ&sortby=pubdate
Orcid id	https://orcid.org/0000-0002-5804-7296
Scopus	https://www.scopus.com/authid/detail.uri?authorId=57202080511
Publons	https://www.webofscience.com/wos/author/record/AAW-8373-2021
Research Gate	https://www.researchgate.net/profile/Venkata-Thota-2/stats
Vidwan-Inflibnet	https://vidwan.inflibnet.ac.in/profile/197505
Web of science	https://www.webofscience.com/wos/author/record/AAW-8373-2021

Papers Published in Conferences: 10

1. **T. Venkata Lakshmi**, M.Kamaraju, Implementation of High Performance 6T-SRAM Cell" High Performance 6T-SRAM Cell", Journal of Physics: Conference Series, doi:10.1088/1742-6596/1804/1/012185(**Scopus**).
2. Surapaneni Aparna, Y. Sri Chakrapani, **T. Venkata Lakshmi**, M.Kamaraju, "A Survey of VLSI Architectures for Hamming Code Algorithms", International Journal of Advanced Scientific Research and Management, CITAES-18, ISSN:2455-6378.
3. Ch.S.DayaAdarsh, **T.Venkata Lakshmi**, M.Kamaraju "Implementation and Comparative Analysis of Double Gate Low Power Multiplexers Using Dynamic Logic Styles", International Conference on Electronics, Communication and Aerospace Technology, ICECA 2017, IEEE Xplorer April 2018, DOI: 10.1109/ICECA.2017.8212825
4. PallaviSesham, **VenkatalakshmiThota**, Kamaraju M, "Design Of Kogge-Stone Adder Using Adiabatic Logic", National Conference on Advances in Electrical, Electronics & Computer Engineering (NCAEECE) in association with IRAJ Research Forum and Institute of Research and journals (IRAJ), April 2018.
5. **T. Venkata Lakshmi**, R. Trinadh, "A novel design of Area and Power Efficient high speed data path logic system", International conference on Systemics, Cybernetics and Informatics (ICSCI-2014), under the aegis of Pentagram Research Centre, India in association with IEEE Hyderabad section during 3rd-6th February 2014, pp:75-78.
6. **T. Venkata Lakshmi**, M. Kamaraju, P.V. Subba Rao "A Novel Implementation of Voting System using SMS", International Conference on Signal, Image Processing and Pattern Recognition (SIPP-2013) at Bangalore during February 18-19, 2013.
7. M. V. Phani Kumar, **T. Venkata Lakshmi**, "Impulse noise removal from color image sequences using Fuzzy Filter", of International conference on Computer Science and Information Technology (CSIT-2012) at Bhopal on 3rd February 2012, pp-41-47.
8. Jayaprakash.P, A.Mallaiah,**T. Venkata Lakshmi**, "VLSI Implementation for Basic ARPS Algorithm for Video Compression", International conference on Technology and Innovation (ICTI-2011), Chennai on 26th July 2011.
9. R. Trinadh, **T. Venkata Lakshmi**, "Enhanced Feature sets for face recognition under difficult lighting conditions", International conference on Nanoscience, Engineering & Advanced Computing (ICNEAC-2011), pp: 64-69 at Narsaraopet during July 8-10, 2011.

10. Ravi Kumar K, **T. Venkata Lakshmi**, “Implementation of Image fusion algorithm based on wavelet transform and curvelet transform for improved quality factors”, International Conference on Nanoscience Engineering & Advanced computing (ICNEAC-2011) at Narsaraopet during July 8-10, 2011.

Workshops Organized: 02

1. One week ISTE STTP on “CMOS, Mixed Signal and Radio Frequency VLSI Design”, organized by IIT Kharagpur under the MHRD from 30th January-4th February, 2017.
2. Two day training programme on “**Advances in VLSI Design and Embedded Systems**” organized by VLSI & Embedded Systems Research group, department of ECE,

Workshops /Training Programs/Seminars Attended: 33

1. A one week FDP on “**Optimized Solutions for IC Design with hands-on session on CMOS Design**” Electronics & ICT Academy, NIT Warangal in association with Jawaharlal Nehru Technological University Kakinada (Spoke Academy) from 3rd-7th February 2025.
2. The three-day Online Faculty Development Program on “**Generative AI**” organized by the Department of Information Technology of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 26th June 2024 to 28th June 2024.
3. A One week Refresher course on “**Transmission lines and Waveguides**” organized by Department of ECE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 29th January -2nd February, 2024.
4. A One week Refresher course on “**Microcontrollers and Embedded Systems**” organized by Department of ECE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 09th -13th January, 2024.
5. A Two day Faculty Development Program on “**Design of Integrated Circuits using Microwind Tools**” organized by Department of ECE, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 30th August, 2023 to 31st August, 2023.
6. A One Week Faculty Development Program on “**Impact of AI: Robotics, VLSI and Medical Domains**” organized by Department of Electronics and Communication

Engineering, Vasireddy Venkatadri Institute of Technology, Nambur, in Association with IETE Vijayawada Centre from 29-01-2024 to 02-02-2024.

7. The online National webinar on “**Low Power VLSI Design Challenges: A View**” on 16-03-2024, organized by VLSI & Microelectronics Research group, Department of ECE, KL University, India.
8. A One week Refresher course on “**Transmission lines & Wave guides**” held at SRGEC from 29 January 2024 to 02 February 2024.
9. A One week Refresher course on “**Microcontrollers and Embedded systems**” held at SRGEC from 09-13 January 2024.
10. AICTE sponsored Online Short Term Training Programme on "**Blockchain Architecture Design and Use Cases**" (Smart Contracts with Ethereum Solidity) Phase-III organized by Department of Electronics and Communication Engineering from 24-08-2020 to 29-08-2020.
11. A National level online Faculty Development Program on "**Cyber Security**" organized by the Departments of Computer Science and Engineering & Information Technology in association with CyberPsy, Gudlavalleru Engineering College, Gudlavalleru from 22nd-26th July, 2020.
12. One week Faculty Development Program on "**Recent Advances in Biomedical Applications and Communication Networks**" organized by the department of Electronics and Communication Engineering, GMR Institute of Technology, Rajam, AP, India during 13th-18th.
13. This is to certify that Thota. Venkata Lakshmi from Gudlavalleru Engineering College has participated in the “**National level quiz on Hardware Description Language**” conducted by the Department of ECE, NRI Institute of Technology on 7/14/2020 and secured 70% score.
14. A FIVE Day National Level Online Faculty Development Program on “**Trends in Electronics & Communication Engineering**” conducted during 22nd-26th June 2020, Organized by Department of Electronics & Communication Engineering, Sri Vasavi Engineering College, Tadepalligudem, Andhra Pradesh.

15. Participated in webinar titled “**Research Issues and Career Opportunity in VLSI Design**” organized by IEEE Student chapter (STB10084) of K.Ramakrishnan College of Technology, Samayapuram, Trichy on 25.06.2020.
16. Successfully completed "**Matlab Masters**" course from 8-6-2020 to 19-06-2020 conducted by Data Teach Labs.
17. Participated in webinar for two days entitled “**CMOS circuit design in Cadence virtuoso in 180nm**” organized by IEEE-Student Branch (STB 10084) of K.Ramakrishnan College of Technology, Samayapuram, Trichy on 19th & 20th June 2020.
18. One week International online Knowledge Development Program on "**Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: A Researchers View**" organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru from 8th-13th June 2020.
19. One week national level online Faculty Development Program on "**Research Challenges and Emerging Trends in Electronics and Communication Engineering**", from 27 May 2020 to 1st June 2020.
20. Three Day National Level WEBINAR SERIES on “**VLSI**”, organized by the Department of Electronics and Communication Engineering, from 10th to 12th June 2020 in collaboration with APPLY VOLT.
21. Participated in the Quiz on "**Faculty Awareness On NBA**" conducted by the Department of Electrical And Electronics Engineering, Malla Reddy College of Engineering and Technology, Telangana and secured 60% score.
22. One Week Online FDP on "**Recent Trends and Research Areas in Applied VLSI**" and Advanced Communications” organized by the Department of ECE, VVIT from 08th June to 12th June 2020.
23. Successfully completed a “**Skill test on VLSI Design**” on 6/7/2020 Organized by Srinivasa Institute of Engineering and Technology.
24. One week online FDP on "**Recent Trends and Research Areas in Applied VLSI and Advanced Communications**" organized by Department of Electronics and Communication Engineering Vasireddy Venkatadri Institute of Technology, Nambur, Guntur (Dt.), Andhra Pradesh.

25. Participated in the webinar on “**Global trends in Biomedical Teaching and Research**” organized by Chennai Institute of Technology on 5th June 2020.
26. participated in webinar on “**Overview on VLSI**” conducted by Departments of R&D and ECE on 4th June, 2020 organized by GEETHANJALI Institute of Science & Technology, Nellore, Andhra Pradesh 524137,
27. One week FDP on "**Artificial Intelligence and its Applications**" conducted by Department of Computer Science and Engineering, Ramachandra College of Engineering, Eluru in collaboration with 360EDigiTMG from 25th May 2020 to 30th May 2020.
28. A Three Day National Level Online Faculty Development Program on "**Research Trends in Signal Processing, Antennas, VLSI & IoT**" during 28th - 30th May 2020, Organized by The Department of Electronics & Communication Engineering, Sri Vasavi Engineering College(Autonomous) Tadepalligudem, Andhra Pradesh 534101.
29. Excellent performance in TechQuip – “**A National Level Technical E Quiz series Season-1**” during 20th May to 25th May-2020 organized by Department of Electronics & Communication Engineering of Sri Venkatesa Perumal College of Engineering & Technology (AUTONOMOUS), Puttur.
30. Participated a webinar on “**Radio Frequency Measurements for Cellular and Wireless Communication Systems**” organized by Department of Electronics and Communication Engineering on 25th May, 2020 organized by ECE Department Gudlavalleru Engineering College, Gudlavalleru,
31. Successfully completed an Online Faculty Awareness Program on “**Research Methodology**” organized by Department of Mechanical Engineering, Rajgad Dnyanpeeth’s Shri Chhatrapati Shivajiraje College of Engineering, Pune-412206 during the period of 20/05/2020 to 25/05/2020 with a score of 89%.
32. Participated in workshop on “**VLSI Circuits Analog & Digital Design Perspectives**” conducted by Department of ECE in association with AMS Semiconductors and HCL Technologies from 16/5/2020 to 17/5/2020.
33. Participated in a Webinar on “**Research Publications, Process, Credentials and Outcomes**” on 9th May 2020 organized by Baba Institute of Technology and Sciences.

34. One week FDP on "**ICT Tools**" in association with STP-IIT Bombay during 11th-16th May 2020 organized by Department of IT, Sree Vidyanikethan Engineering College, Tirupati.
35. One week FDP on "**Data Acquisition and RT using LabVIEW**" organized by ECE Department Gudlavalleru Engineering College, Gudlavalleru in Association with National Instruments, Bangalore from 11th to 13th November, 2019.
36. A three day training program on "**Advanced Development Tools Based IoT**" organized by ECE Department, Gudlavalleru Engineering College, Gudlavalleru from 11th to 13th November, 2019.
37. Attended a 12 week NPTEL-AICTE FDP on "**Microelectronics: Devices to Circuits**" during July-November, 2019.
38. Attended a 8 week NPTEL-AICTE FDP on "**CMOS Digital VLSI Design**" during February-April, 2018.
39. Two day training programme on "**Advances in VLSI Design and Embedded Systems**" organized by VLSI & Embedded Systems Research group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 31st August and 1st September, 2017.
40. Short course on "**Modeling and Simulation of Nano-Transistors**" organized by Department of Electrical Engineering, IIT Kanpur during 13th -17th February, 2017.
41. One week National Level FDP on "**Signal and Image Processing using LabVIEW**" during 13th -18th November, 2017 organized by Signal Processing Research group of department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with NI Systems Pvt. Ltd.
42. A one week faculty development programme on "**Hands on experience in Signal Processing Applications**" organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru from 7th -12th November, 2016.
43. A one week faculty development programme on "**Development of Wireless Sensor Networks using Network Simulator (NS2)- Hands on experience**" organized by Communications Research group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with Vertex Soft, Hyderabad from 1st-5th December, 2015.

44. One week ISTE STTP for co-ordinators on **“CMOS, Mixed Signal and Radio Frequency VLSI Design”** conducted by IIT Kharagpur under the MHRD from 19th-23rd September, 2016.
45. Thirty second National Convention of Electronics and Telecommunication Engineers and National seminar on **“Intelligent System for More Connected World: Internet of Things”** organized by The Institution of Engineers (India), Andhra Pradesh State Centre during 24th -25th October, 2016.
46. A one week faculty development programme on **“Development of Wireless Sensor Networks using Network Simulator (NS2)- Hands on experience”** organized by Communications Research group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with Vertex Soft, Hyderabad from 1st-5th December, 2015.
47. One week refresher course on **“Microprocessors & Microcontrollers”** organized by Research & Development center, department of ECE, Gudlavalleru Engineering College, Gudlavalleru, during 17-21 November, 2015.
48. A five day continuing education and training programme on **“Low Power System on Chip Design using XILINX FPGA Devices”** organized by the department of ECE, NIT Warangal during 3rd -7th October, 2015.
49. A two day All India workshop on **“Emerging Techniques on VLSI Architecture Design for real time environment”** conducted by department of ECE, PACE institute of technology & sciences, Ongole organized by The Institution of Engineers (India), Vijayawada Local Centre during 9th -10th October, 2015.
50. One day workshop on **“Research & Development Facilitation”** organized by The Institute of Engineers (India), Vijayawada Local Centre, Vijayawada on 29th March, 2015.
51. A two day All India Seminar on **“Agricultural and Horticultural Applications of Unmanned Air Vehicles for Micro-Farming”** organized by The Institute of Engineers (India)-Vijayawada Local Centre along with Shri Vishnu Engineering College for women, Bhimavaram and National Design and Research Forum, Bangalore during 26th - 27th March, 2015.
52. A three day National workshop on **“Theory & Applications of Intelligent Signal Processing”** organized by Signal Processing & Communications group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 28th February-2nd March, 2015.

53. A five day Short term course on “**Advanced DSP Design Techniques**” organized by IIT Kharagpur under Continuing Education Programme during July 6th -10th, 2015. Two week audit course on “**Research Methodology**”, organized by JNTUK, Kakinada during 1st - 15th May 2015.
54. Two week audit course on “**Intellectual Property Rights and Patents**”, organized by JNTUK, Kakinada during 16th - 30th May 2015.
55. A one day workshop on “**Ultra Low Power Communication using CC430 Microcontroller::Hands on Experience**” organized by Signal Processing & Communications group, department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 7th February, 2015.
56. A one day seminar on “**Research in signal and image processing on FPGA & Embedded platforms**” organized by The Institute of Engineers (India), Vijayawada Local Centre, Vijayawada on 22nd November 2014.
57. The value added programme on “**Digital IC Design using CADENCE Tools**” organized by the school of Electronics Engineering, VIT Chennai from 21st -23rd August, 2014.
58. One day workshop on “**Speech Processing: Current Challenges and Hands-on Experience**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.
59. Two week ISTE workshop on “**Signals & Systems**” conducted by IIT Kharagpur in association with department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 2nd -12th January 2014.
60. A two day workshop on “**Issues In Integrated Circuits (ICs) Design**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 13th -14th December, 2013.
61. A two day ISTE workshop on “**Aakash for Education**” conducted by IIT Bombay under the National Mission on Education through ICT (MHRD) on 10th&11th November, 2012.
62. Faculty Development Program on “**VLSI and Embedded System Design**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 7th – 8th July 2012.

63. One week refresher course on “**Pulse & Digital Circuits (Theory & Lab Practice)**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 21st – 26th November 2011.
64. A five day Training Programme on “**Mentor Graphics (Frontend & Backend) and XILINX Tools**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with IETE Vijayawada Centre & IETE Students’ Forum (ISF-GEC) and CoreEL Technologies, Bangalore from 13th – 17th September 2010.
65. A two week AICTE sponsored Staff Development Program on “**Current Trends in Biometrics**” organized by the department of IT, Gudlavalleru Engineering College, Gudlavalleru during 2nd -14th November, 2009.
66. A two day workshop on “**Verilog Programming & Implementation**” organized by department of ECE and IEEE Student branch of Gudlavalleru Engineering College, Gudlavalleru during 27th -28th October, 2007.
67. A two day workshop on “**VLSI Design**” organized by department of ECE and IEEE Student branch of Gudlavalleru Engineering College, Gudlavalleru during 9th -10th February, 2007.

GUEST LECTURES DELIVERED: 3

1. Delivered a Guest Lecture on “**Basic Electronic Circuits and Instrumentation**” in one week Refresher Course at SRGEC on 03 February 2024.
2. Delivered a talk on “**Over View on Blooms Taxonomy**” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru under Academic Strengthening and Advancement on 15th March, 2018.
3. Delivered CLT classes on “**VLSI Design**” for III B.Tech II sem ECE students during the academic year 2016-17.
4. Delivered a lecture on “**Multivibrators**” as part of One week refresher course on “Pulse & Digital Circuits (Theory & Lab Practice)” organized by department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 21st – 26th November 2011.

Subjects Handled:

- Electronic Devices & Circuits

- Control Systems
- Electronic Measurements and Instrumentation
- Bio Medical Engineering
- Analog Circuits
- Signals & Systems
- Basic Electronics
- Analog Electronics
- Elements of Electronic Engineering
- Digital Circuit Design
- VLSI Design
- Low Power VLSI Design
- RADAR Engineering

Other Responsibilities

College Level

1. NAAC C2 incharge
2. IQAC Member

Department Level

1. Associate Mentor
2. CMC Chairman for I B.Tech ECE
3. VLSI Design research group Coordinator
4. NBA – Criteria 5 incharge
5. Student Counsellor
6. BOS Member
7. Subject expert Team

BOOKS PUBLISHED IN PUBLICATIONS:

1. Co-author of book entitled “Electronics and Communication Engineering laboratory Manual” with ISBN – 978-1-4276-5536-3.
2. Co-author of book entitled “Electronics and Communication Engineering Instructional Material” with ISBN – 978-1-4276-5539-4.

NPTEL COURSE CERTIFICATIONS: 03

S.No	Course	Month & Year	Grade
1	Micro Electronics-From Devices to Circuits”	January-April 2019.	Successfully completed
2	CMOS Digital VLSI Design”	Feb-Apr 2019	Elite
3	Digital Electronic Circuits	January-April 2019.	Elite

MEMBERSHIP IN PROFESSIONAL BODIES:

1. Executive Committee Member, Institution of Electronics And Telecommunication Engineers (IETE), Vijayawada center during 2024-26.
2. Fellow of the Institution (FIETE) No. F-503646.
3. Member of IE No. M150337-5
4. Life time member of ISTE – LM48530
5. SLT LM476
6. Member of Edwin Group of Journal (Editorial Member)

Signature of the Faculty
(T.VENKATA LAKSHMI)